

Embedded Storage elements on next MCU generation ready for AI at the edge

Challenges and objectives

First 'silicon embedded' AI MCU hardware components ready for large volume AI at the edge applications.

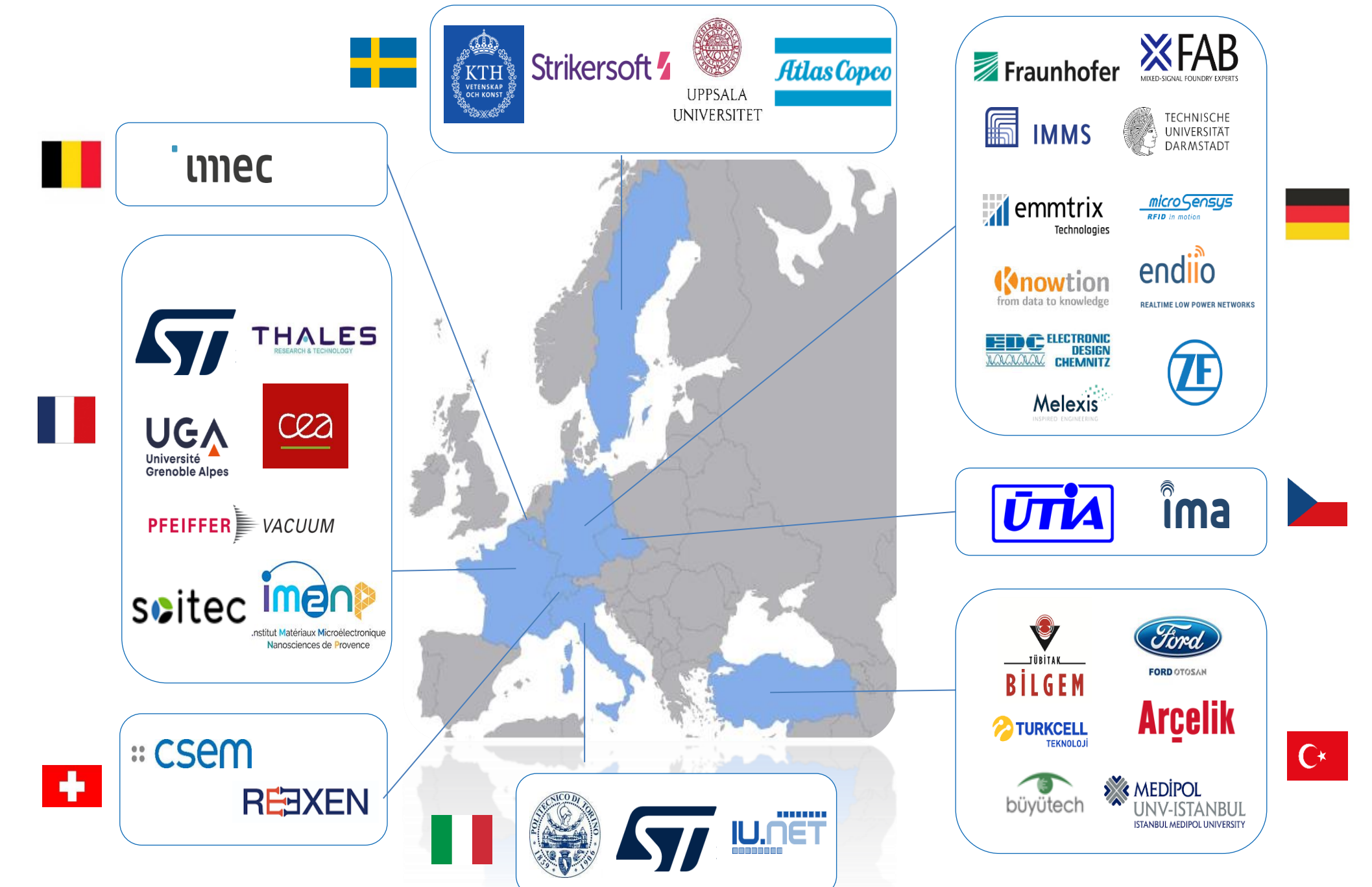
StorAige will increase AI technology maturity and capitalize on 'the ones' who already have competences and insights to push one step up the full European eco-system and stay on/trust podiums!

Technical goals

The main aim of the StorAige project is to setup a world-class manufacturing platform for silicon with Artificial Intelligence capabilities, prototyping high performance, FD-SOI low power and secured & safety components enabling competitive AI at the edge applications.

StorAige project will put together experts to share insights, concepts, ideas, experiments, studies, to develop 'appropriate' and standard-grade AI solutions.

Three main applications areas will be targeted and demonstrated within the scope of this project: Automotive, Industrial and Secure



Consortium: 40 partners

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The StorAige work plan structure follows "a value chain like" approach.

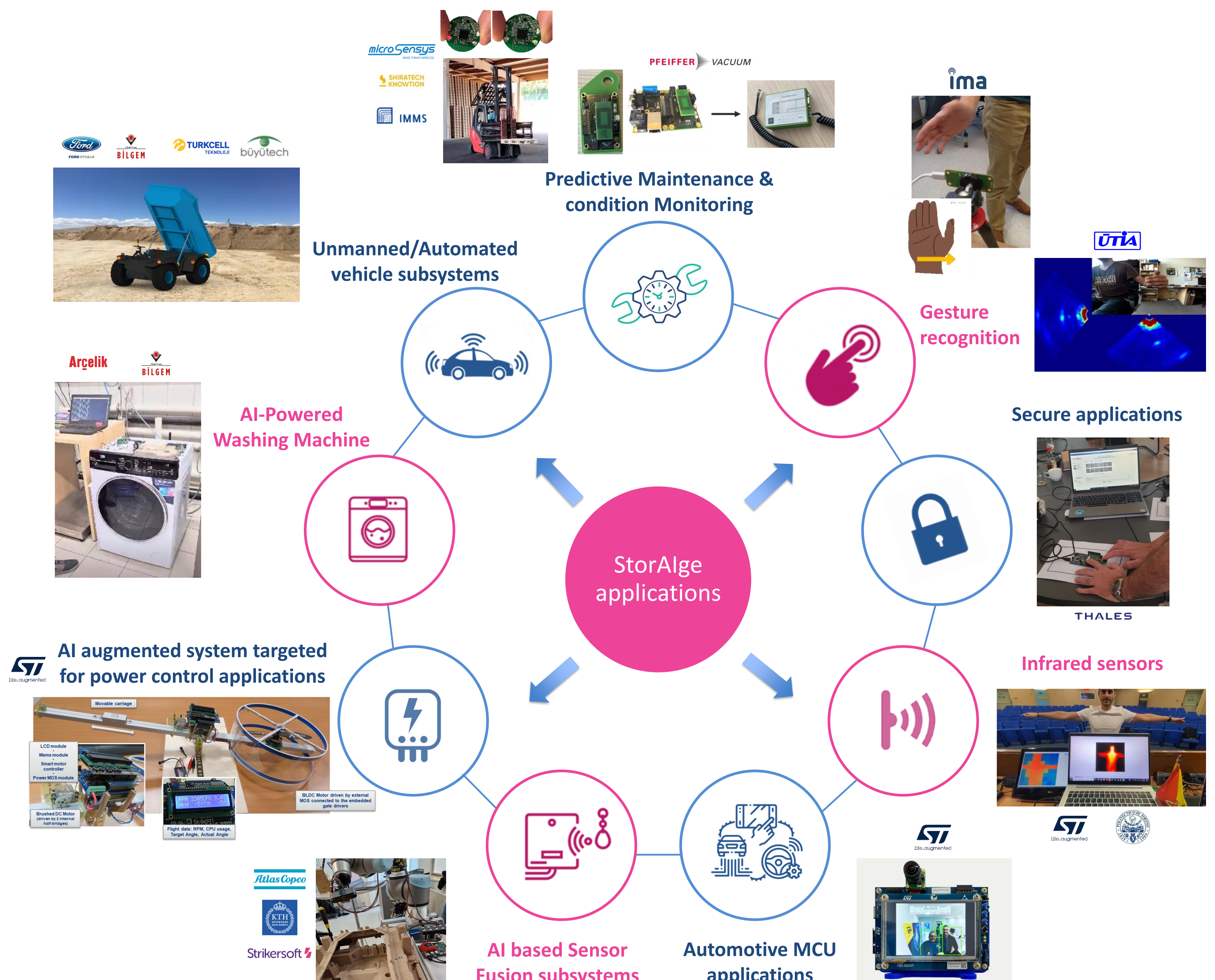
Expected impact

By providing the best-in-class silicon-based solutions and joining forces of the AI value chain in the EU, StorAige will help:

- to predict and define the tasks to which AI will be applied in edge devices tomorrow
- to support their widespread adoption
- Europe to maintain strong competitiveness and sovereignty

Moving AI from the cloud to the edge will orient the technological developments towards three interdependent challenges:

- Increase the computing power (high performance),
- Lower the energy consumption (energy-efficient),
- Implement adequate security & privacy level.



StorAige : 9 applications, 21 use-cases