

NanoIC

Accelerating chip innovation beyond 2 nm

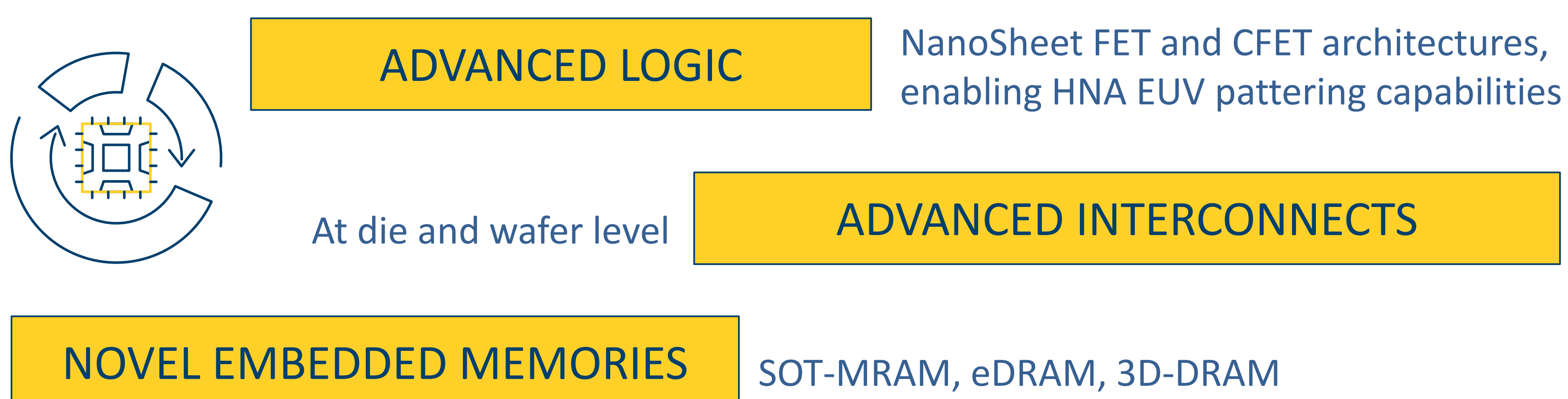
Challenges and objectives

The NanoIC project aims to fulfil the European Chips Act's vision for leadership and competitiveness in **advanced system-on-chip (SoC) innovation**.

Critical markets requiring **high integration and efficiency** will be strongly impacted by these technologies → high-performance computing, energy, AR/VR, automotive, and healthcare.

Technical goals

R&D enablement to prepare the relevant **platforms** in the field of



With the aim to **develop, optimize and mature baseline flows**, enabling the **relevant technical capability** while strongly increasing repeatability, reducing variability and defectivity → towards platforms to enable **early access**.

Expected impact

- **PDKs** for virtual prototyping of new system architectures and **prototyping** using foundry logic wafers;
- **Training initiatives and collaborations** with universities and research organizations.

European outreach to all actors in the supply chain:



→ **European competitiveness across the entire industry value chain:** from semiconductor materials and processes, to designs, systems, and applications.



NanoIC

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Meet the NanoIC consortium



Additional information

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